IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Art Unit:

Not assigned

Yasuo OKADA

Examiner: Not assigned

Serial No: Not assigned

Filed: July 15, 2003

For:

MANUFACTURING METHOD OF SEMICONDUCTOR

DEVICE AND SEMICONDUCTOR DEVICE

TRANSMITTAL OF INFORMATION DISCLOSURE **STATEMENT**

Mail Stop PATENT APPLICATION Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

The information disclosure statement submitted herewith is being filed concurrently with the subject application [37 C.F.R. § 1.97(b)] and contains no items of information cited in any communication from a foreign patent office in a counterpart foreign application [37 C.F.R. § 1.97(e)(1)].

If it should be determined that for any reason either an insufficient or excessive fee has been paid, please charge any insufficiency or credit any overpayment necessary to ensure consideration of the information disclosure statement for the above-identified application to Deposit Account No. 50-1314. A copy of this paper is enclosed.

Respectfully submitted,

Date: July 15, 2003

Anthony J. Orler Registration No. 41,232 Attorney for Applicant(s)

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Facsimile: 213-337-6701

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INFORMATION DISCLOSURE CITATION IN AN APPLICATION (Use several sheets if necessary)			Docket Number (Optional) 81788.0253		Application Number Not assigned	
			Applicant Yasuo OKADA			
			Filing Date July 15, 2003		Group Art Unit Not assigned	
		U.S. PATEN	T DOCUMENTS			
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